

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al. Art Unit : 2154
Serial No. : 10/069,229 Examiner : Joshua Joo
Filed : December 11, 2002 Conf. No. : 1429
Title : BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE

MAIL STOP AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

NOTICE OF APPEAL

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the action dated March 17, 2006, finally rejecting claims 1-21.

A Petition for an Extension of Time under 37 CFR § 1.136 to extend the time to respond to the final rejection for 1 month to and including July 17, 2006 is enclosed.

The fees in the amount of \$500 and \$120 are being paid concurrently on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other required fees to deposit account 06-1050, referencing the attorney docket number shown above.

Respectfully submitted,

Date:

July 14, 2006

Ido Rabinovitch

Ido Rabinovitch
Attorney for Intel Corporation
Reg. No. L0080

PTO Customer No. 20985
Fish & Richardson P.C.
Telephone: (617) 542-5070
Facsimile: (617) 542-8906